Fig.1

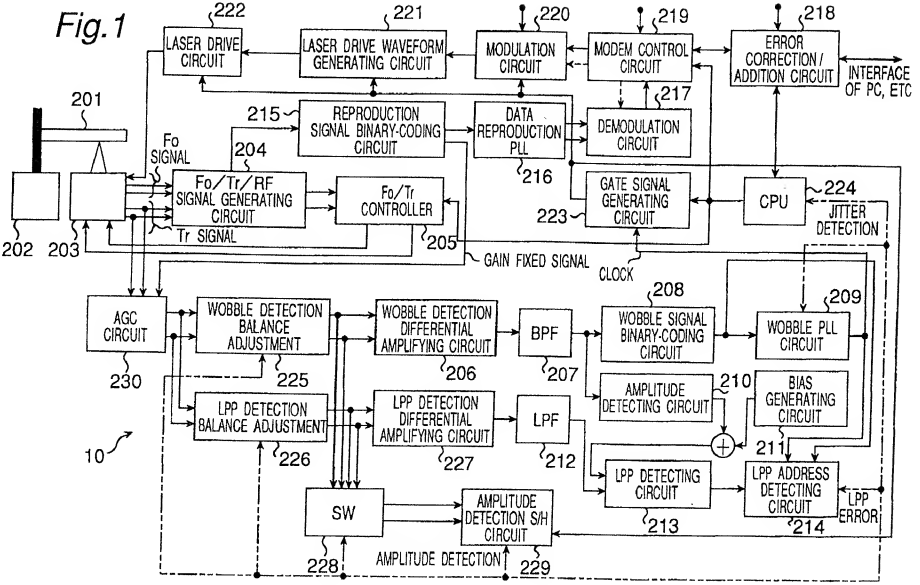
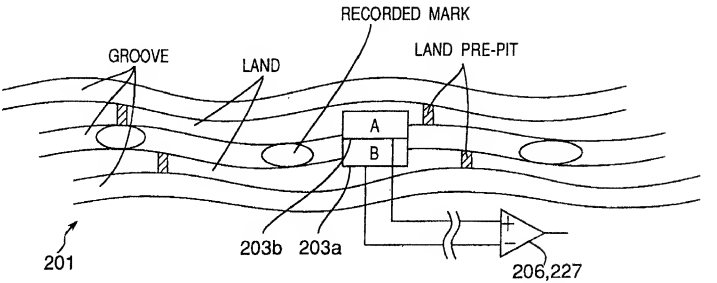


Fig.2



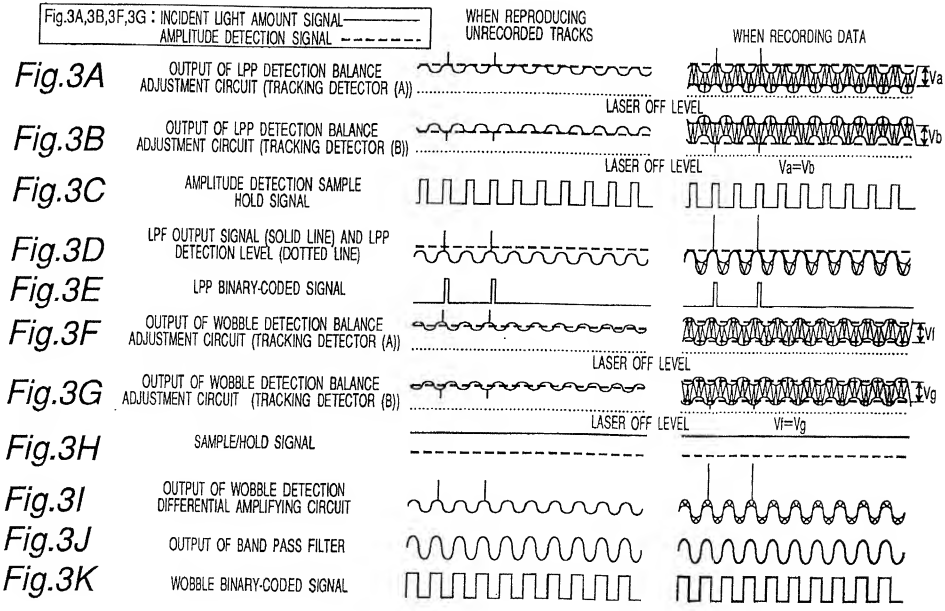


Fig.4

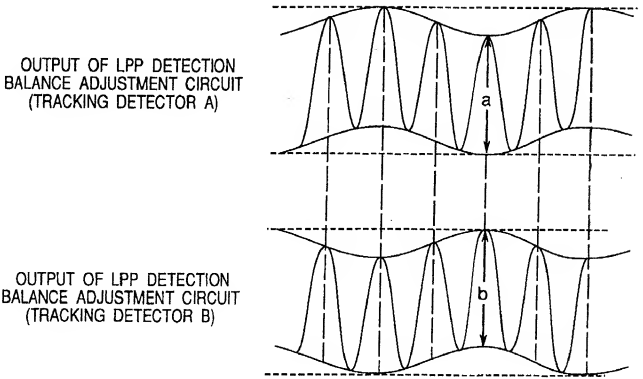

OUTPUT OF LPP DETECTION BALANCE ADJUSTMENT CIRCUIT (TRACKING DETECTOR A)
a
OUTPUT OF LPP DETECTION BALANCE ADJUSTMENT CIRCUIT (TRACKING DETECTOR B)
b
a=b

Fig.5A,5B : INCIDENT LIGHT AMOUNT SIGNAL ——— AMPLITUDE DETECTION SIGNAL - - - - - -

WHEN REPRODUCING RECORDED TRACKS

Fig.5A

OUTPUT OF LPP DETECTION BALANCE ADJUSTMENT CIRCUIT (TRACKING DETECTOR (A))

Fig.5B

OUTPUT OF LPP DETECTION BALANCE ADJUSTMENT CIRCUIT (TRACKING DETECTOR (B))

Fig.5C

AMPLITUDE DETECTION SAMPLE/HOLD SIGNAL

Fig.5D

LPF OUTPUT SIGNAL (SOLID LINE) AND LPP DETECTION LEVEL (DOTTED LINE)

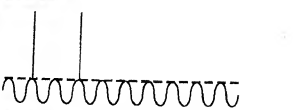


Fig.5E

OUTPUT OF BAND PASS FILTER

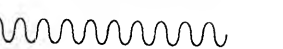


Fig.5F

WOBBLE BINARY-CODED SIGNAL

Fig.5G

LPP BINARY-CODED SIGNAL

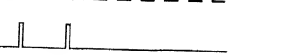


Fig.6

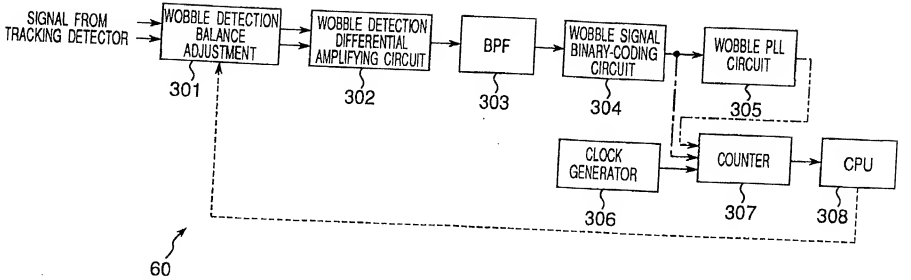
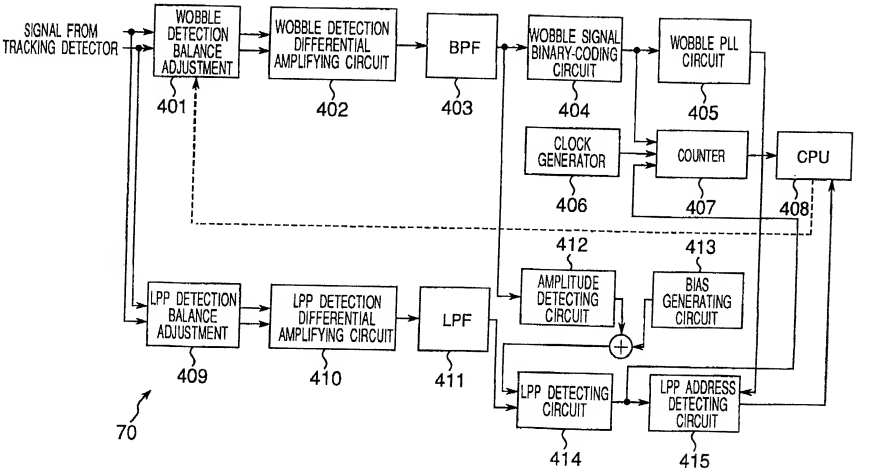

SIGNAL FROM TRACKING DETECTOR
WOBBLE DETECTION BALANCE ADJUSTMENT
301
WOBBLE DETECTION DIFFERENTIAL AMPLIFYING CIRCUIT
302
BPF
303
WOBBLE SIGNAL BINARY-CODING CIRCUIT
304
WOBBLE PLL CIRCUIT
305
CLOCK GENERATOR
306
COUNTER
307
CPU
308
60

Fig.7


SIGNAL FROM TRACKING DETECTOR
WOBBLE DETECTION BALANCE ADJUSTMENT
401
WOBBLE DETECTION DIFFERENTIAL AMPLIFYING CIRCUIT
402
BPF
403
WOBBLE SIGNAL BINARY-CODING CIRCUIT
404
WOBBLE PLL CIRCUIT
405
CLOCK GENERATOR
406
COUNTER
407
CPU
408
412
413
LPP DETECTION BALANCE ADJUSTMENT
409
LPP DETECTION DIFFERENTIAL AMPLIFYING CIRCUIT
410
LPF
411
AMPLITUDE DETECTING CIRCUIT
BIAS GENERATING CIRCUIT
LPP DETECTING CIRCUIT
414
LPP ADDRESS DETECTING CIRCUIT
415
70

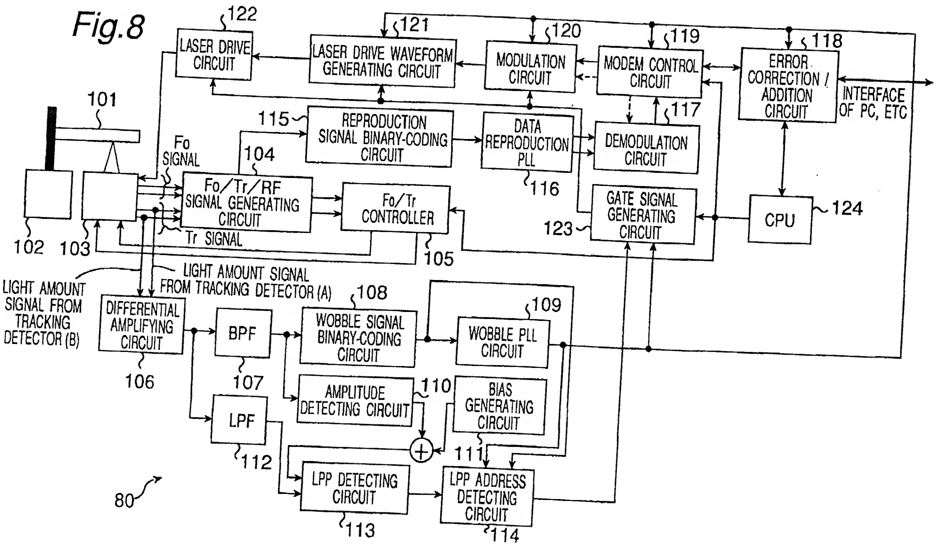

122
LASER DRIVE CIRCUIT
121
LASER DRIVE WAVEFORM GENERATING CIRCUIT
120
MODULATION CIRCUIT
119
MODEM CONTROL CIRCUIT
118
ERROR CORRECTION / ADDITION CIRCUIT
INTERFACE OF PC, ETC
101
Fo SIGNAL
115
REPRODUCTION SIGNAL BINARY-CODING CIRCUIT
DATA REPRODUCTION PLL
117
DEMODULATION CIRCUIT
104
Fo/Tr/RF SIGNAL GENERATING CIRCUIT
Fo/Tr CONTROLLER
116
GATE SIGNAL GENERATING CIRCUIT
CPU
124
123
102
103
Tr SIGNAL
105
LIGHT AMOUNT SIGNAL FROM TRACKING DETECTOR (A)
LIGHT AMOUNT SIGNAL FROM TRACKING DETECTOR (B)
DIFFERENTIAL AMPLIFYING CIRCUIT
106
BPF
107
108
WOBBLE SIGNAL BINARY-CODING CIRCUIT
109
WOBBLE PLL CIRCUIT
AMPLITUDE DETECTING CIRCUIT
110
BIAS GENERATING CIRCUIT
LPF
112
111
LPP DETECTING CIRCUIT
113
LPP ADDRESS DETECTING CIRCUIT
114
80

Fig.8